## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Inventor:

Jong-Jan Lee, et al.

Serial No.:

10/625,065

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PATENT APPLICATION

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Docket No.

**SLA0696** 

Group No.

1765

Confirmation No.

8265

Entitled:

FABRICATION OF SILICON-ON-NOTHING (SON) MOSFET

FABRICATION USING SELECTIVE ETCHING OF SII-X GEX

LAYER

STATUS INQUIRY

MAR 2 1 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

It has been over 18 months since the filing of this application and applicant has not yet received an Office Action or other response on the merits of this application. Accordingly, applicants' attorneyof-record, whose name and address appear below, hereby inquires as to the status of the above-identified application. Please inform the attorney-of-record of the status of this application by facsimile or letter at the contact information below. Thank you for your attention to this matter.

By:

Respectfully submitted,

121/05

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CERTIFICATE OF FACSIMILE TRANSMISSION

UNDER 37 C.F.R. § 1.8

David C. Ripma, Reg. No. 27,672

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office under 37 C.F.R. §1.8 at Fax No. (703) 872-9306 on March 21, 2005.

Te)vola

Victoria A. Woods